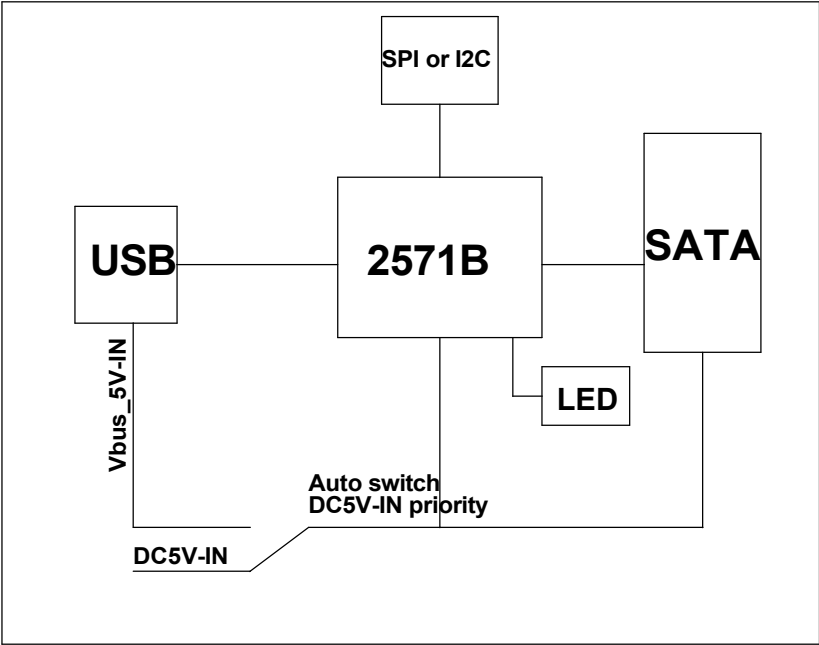



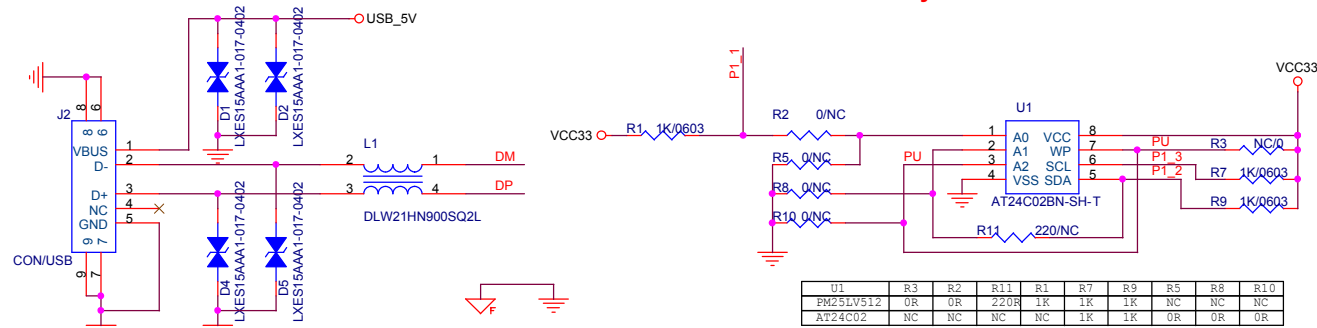
Version	Date	Description	Author
V1.0	2010/03/18	First Released	H.P.-Shen



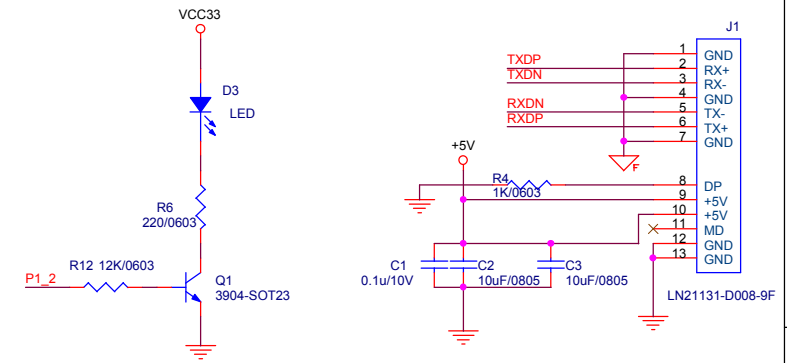
Page 1: Revision History & Block Diagram  
 Page 2: PL2571B, USB and SATA

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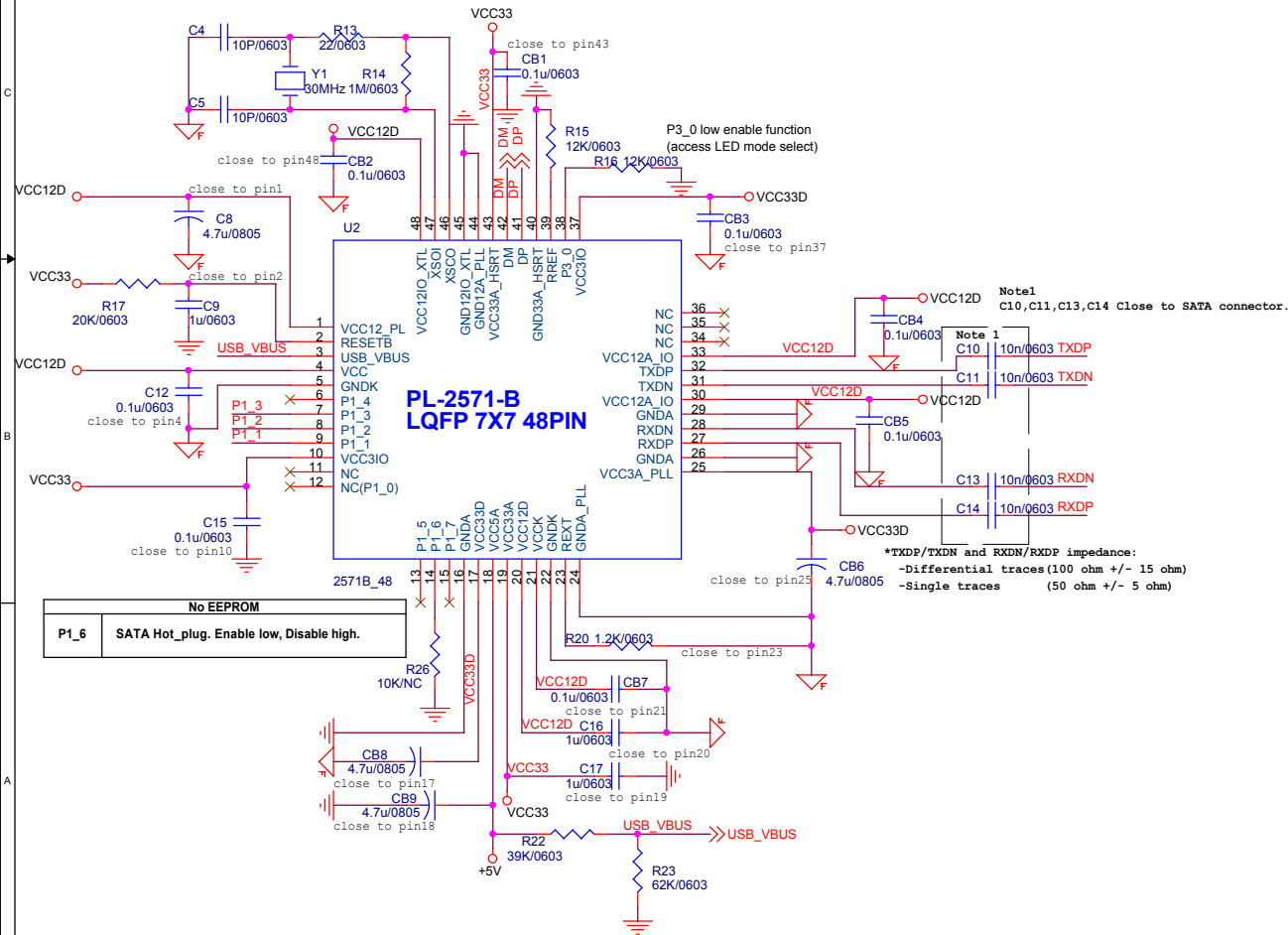
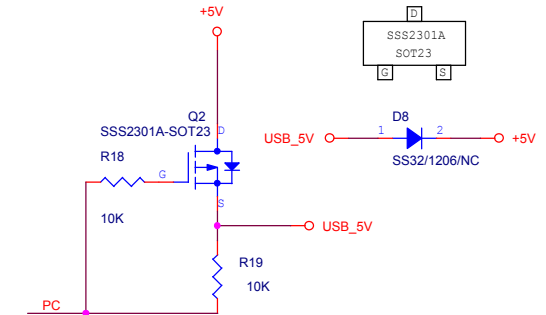
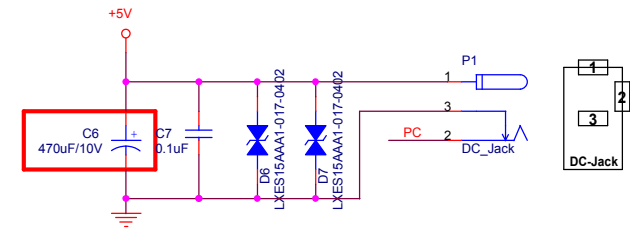
**Please refer to the Layout Guide for detail.**



U1	R3	R2	R11	R1	R7	R9	R5	R8	R10
PM25LV512	0R	0R	220R	1K	1K	1K	NC	NC	NC
AT24C02	NC	NC	NC	NC	1K	1K	0R	0R	0R



**If the total current of Vbus is over 1.5A, the value of C6 must be over 220uF**



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