
** API specification for PL2303TB

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** The document should refer to Sample_PL2303TBDlg.cpp

**

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- **int PL2303_12GPIO_ENABLE(HANDLE hdrv, __int8 GPIO_Num, BOOL Enable)**

PL2303TB has 12 software controllable “General Purpose Input Output” control signal pin, you can use this API to control the direction of these pin.

Parameters

hdrv	[in] Handle to the communication device.
GPIO_Num	[in] The GPIO number, the range is 0 ~11.
Enable	[in] True is Output mode, false is input mode.

Returns

Zero indicates success.
Nonzero indicates failure.

- **int PL2303_12GPIO_SetValue(HANDLE hdrv, __int8 GPIO_Num, BYTE val)**

PL2303TB has 12 software controllable “General Purpose Input Output” control signal pin, you can use this API to control the status of these pin when the direction is output mode.

Parameters

hdrv	[in] Handle to the communication device.
GPIO_Num	[in] The GPIO number, the range is 0 ~11.
val	[in] “1” is High level, 0 is low level.

Returns

Zero indicates success.
Nonzero indicates failure.

- **int PL2303_12GPIO_GetValue(HANDLE hdrv, __int8 GPIO_Num, BYTE *val)**

PL2303TB has 12 software controllable “General Purpose Input Output” control signal pin, you can use this API to get the status of these pin when the direction is input mode.

Parameters

hdrv	[in] Handle to the communication device.
GPIO_Num	[in] The GPIO number, the range is 0 ~11.
val	[out] “1” is High level, 0 is low level.

Returns

Zero indicates success.
Nonzero indicates failure.

- **int PL2303_Enable_Clock_Output(HANDLE hdrv, __int8 Clock_IO_Num, BOOL Enable)**

PL2303TB has 2 pin(GP0 & GP1) to support clock output function, you can use this API to enable clock output function.

Parameters

hdrv [in] Handle to the communication device.
 Clock_IO_Num [in] 0 is GP0 pin, 1 is GP1 pin
 Enable [out] "1" is enable, 0 is disable.

Returns

Zero indicates success.
 Nonzero indicates failure.

- **int PL2303_SetClock(HANDLE hdrv, __int8 Clock_IO_Num, BYTE val)**

PL2303TB has 2 pin(GP0 & GP1) to support clock output function, you can use this API to set clock output frequency(1.5 ~ 48 MHz)

Parameters

hdrv [in] Handle to the communication device.
 Clock_IO_Num [in] 0 is GP0 pin, 1 is GP1 pin
 val [in]

val	Description
0	1.5 Mhz
1	3 MHz
2	6 MHz
3	12 MHz
4	24 MHz
5	48 MHz

Returns

Zero indicates success.
 Nonzero indicates failure.

- **int PL2303_Enable_LED_Output(HANDLE hdrv, __int8 LED_IO_Num, BOOL Enable)**

PL2303TB has 2 pin(GP0 & GP1) to support LED display control during Tx/Rx is active, you can use this API to enable LED display.

Parameters

hdrv [in] Handle to the communication device.
 LED_IO_Num [in] "0" is GP0 pin, "1" is GP1 pin
 Enable [in] "1" is enable, "0" is disable.

Returns

Zero indicates success.
 Nonzero indicates failure.

- **int PL2303_SetLED(HANDLE hdrv, __int8 LED_IO_Num, BYTE val)**

PL2303TB has 2 pin(GP0 & GP1) to support LED display control during Tx/Rx is active, you can use this API to enable LED driver select.

Parameters

hdrv [in] Handle to the communication device.
 LED_IO_Num [in] "0" is GP0 pin, "1" is GP1 pin
 val [in]

val	Description
0	Tx LED
1	Rx LED
2	Tx or Rx LED

Returns

Zero indicates success.
 Nonzero indicates failure.

- **int PL2303_Set_PWM_Output(HANDLE hdrv, __int8 PWM_IO_Num, BYTE *buff)**

PL2303TB supports 4 PWM control output, the frequency range is from 366.3Hz to 46.5 KHz, and the pulse high duty can be set from 1/256 to 255/256 in each frequency.

Parameters

hdrv	[in] Handle to the communication device.
PWM_IO_Num	[in] Pin mapping→PWM[0:3] [DTR, DCD, DSR, CTS]
buff	[in] It would set the Frequency with the low byte of its parameter and Duty with the high byte.

Returns

Zero indicates success.
Nonzero indicates failure.